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CERTIFICATE OF Applicant(s): Hussein I.	TRANSMISSION BY FAC Hanafi, et al.	CSIMILE (37 CFR 1.8)	YO	Docket No. DR920020257US1 (15949)				
Application No. 10/639,942	Filing Date August 13, 2003	Examiner Stanetta D. Isaac		Group Art Unit 2812				
Invention: DEVICE THRESHOLD VOLTAGE CONTROL OF FRONT-GATE SILICON- ON-INSULATOR MOSFET USING A SELF-ALIGNED BACK-GATE								
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AMENDMENT TRANSMITTAL LETTER (Large Entity) Applicant(s): Hussein L Hanafi, et al.						Docket No. YOR920020257US1 (15949)		
Application No. 10/639,942	Filing Date August 13, 2003	Examiner Stanetta D. Isaac		Customer 23389	No.	Group Art Unit 2812	Confirmation No. 6974	
Invention: DEVICE THRESHOLD VOLTAGE CONTROL OF FRONT-GATE SILICON-ON-INSULATOR MOSFET USING A SELF-ALIGNED BACK-GATE								
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	CLAIMS REMAINING	HIGHEST # PREV. PAID FOR		R EXTRA		RATE	ADDITIONAL FEE	
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TOTAL ADDITIONAL FEE FOR THIS AMENDMENT \$0.00								
No additional fee is required for amendment.  Please charge Deposit Account No. in the amount of  A check in the amount of to cover the filing fee is enclosed.  The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-0510/IBM  Any additional filing fees required under 37 C.F.R. 1.16.  Any patent application processing fees under 37 CFR 1.47.  Dated: November 5, 2004								
Leslie S. Szivos, Ph.D.  Registration No. 39,394    Certify that this document and fee is being deposited with the U.S. Postal Service as class mail under 37 C.F.R. 1.8 and is addressed to Commissioner for Patents, P.O. Box 1450, Mexandria, 122313-1450.    Signature of Person Mailing Correspondence   Typed or Printed Name of Person Ma								

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### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Hussein I. Hanafi, et al.

Examiner:

Stanetta D. Isaac

Serial No.:

10/639,942

**Art Unit:** 

2812

Filed:

August 13, 2003

Docket: YOR920020257US1 (15949)

Dated:

November 5, 2004

For: DEVICE THRESHOLD VOLTAGE CONTROL OF FRONT-GATE SILICON-

ON-INSULATOR MOSFET USING A SELF-ALIGNED BACK-GATE

Confirmation No. 6974

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

### RESPONSE UNDER 37 C.F.R. § 1.111

Sir:

In response to the Office Action dated August 6, 2004, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

### **CERTIFICATION OF FACSIMILE TRANSMISSION**

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